FPT 2025

https://fpt2025.shanghaitech.edu.cn/

International Conference on Field-Programmable Technology
2nd-5th December 2025, Shanghai, China

FPT'25 Shanghai

Call for Papers

FPT conference is the premier conference in the Asia-Pacific region on field-programmable technologies. Field-programmable devices enable flexible hardware performance with software-like adaptability, driving innovation in high-performance computing, embedded systems, AI accelerators, and beyond.

Important dates
(all at 23:59, UTC+8)

Conference track
- Title and abtract submission due: 14 July 2025
- Paper submission due: 23 July 2025
- Initial reviews/rebuttal questions available: 13 September 2025
- Rebuttal responses due: 20 September 2025
- Notification of acceptance: 11 October 2025

Journal track
- Submission to TRETS due: 16 June 2025
- Initial TRETS reviews available: 28 July 2025
- Revision submission due: 25 August 2025
- Notification of acceptance: 10 October 2025

Submissions are solicited on new research results and detailed tutorial expositions related to field-programmable technologies, including but not limited to:

● Tools and Design Techniques for field-programmable technology including placement, routing, synthesis, verification, debugging, runtime support, technology mapping, partitioning, parallelization, timing optimization, design and run-time environments, high-level synthesis (HLS) compilers, languages and modeling techniques, provably-correct development, intellectual property core-based design, domain-specific development, hardware/software co-design.

● Architectures for field-programmable technology including field-programmable gate arrays, complex programmable logic devices, coarse-grained reconfigurable arrays, field-programmable interconnect, field-programmable analogue arrays, field-programmable arithmetic arrays, memory architectures, interface technologies, low-power techniques, adaptive devices, reconfigurable computing systems, high-performance reconfigurable systems, evolvable hardware and adaptive computing, fault tolerance and avoidance.

● Device technology for field-programmable logic including programmable memories such as non-volatile, dynamic and static memory cells and arrays, interconnect devices, circuits and switches, and emerging VLSI device technologies.

● Applications of field-programmable technology including accelerators for biomedical/scientific/neuro-morphic computing and machine learning, network processors, real-time systems, rapid prototyping, hardware emulation, digital signal processing, interactive multimedia, machine vision, computer graphics, cryptography, robotics, manufacturing systems, embedded applications, evolvable and biologically-inspired hardware.

● Education for field-programmable technology including courses, teaching and training experience, experiment equipment, design and applications.

Note that simply implementing an application using an FPGA is not considered a sufficient research contribution. Application-based papers should emphasize novel design techniques, novel use of embedded resources, or clearly articulated and measured system performance benefits.

Submission Guidelines: FPT 2025 is grateful to the **ACM Transactions on Reconfigurable Technology and Systems (TRETS)** for enabling us to offer a **Journal Track** in addition to regular and short papers directly submitted to the **Conference Track** of FPT. The Journal Track is specifically intended for submissions that would benefit from the longer articles possible in TRETS (up to 32 ACM-style single-column pages). Submissions to the Journal Track that do not take advantage of the additional space offered by TRETS, or deviate significantly from the TRETS Author Guidelines (https://dl.acm.org/journal/trets/author-guidelines), will receive the corresponding feedback early, and can then be revised and entered into the direct FPT submission process.

Details are available at https://fpt2025.shanghaitech.edu.cn/call_for_papers/.